

X-1006-2C US
10/812,525

PATENT
Conf. No.: 3296



IN THE UNITED STATES PATENT OFFICE

Applicant: Stephen M. Trimberger
Assignee: Xilinx, Inc.
Title: A Programmable Logic Device With Time-Multilexed Interconnect
Serial No.: 10/812,525 File Date: 3/29/2004
Examiner: Vibol Tan Art Unit: 2819
Docket No.: X-1006-2C US Conf. No.: 3296

Mail Stop AMENDMENT
COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450

AMENDMENT IN RESPONSE TO FIRST OFFICE ACTION

Dear Sir:

In response to the First Office Action mailed from the Patent Office on February 3, 2005, please replace/substitute the following claims as indicated.

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks begin on page 5 of this paper.